



Attorney's Docket No. 002986.P045

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Kenneth S. McElvain

Application No.: 10/758,977

Filed: January 14, 2004

For: Circuits With Modular Redundancy And  
Methods And Apparatuses For Their  
Automated Synthesis

Examiner: Not Yet Assigned

Art Unit: Not Yet Assigned

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed for applications filed after June 30, 2003). It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

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*Tina Wainright*

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Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):


- X   37 C.F.R. §1.97(b).
- 37 C.F.R. §1.97(c). If so, then enclosed with this Information Disclosure Statement is one of the following:
- A statement pursuant to 37 C.F.R. §1.97(e) or
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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 4/19, 2008

  
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<div style="position: absolute; left: -100px; top: 50px; border: 1px solid black; border-radius: 50%; padding: 5px; transform: rotate(-45deg); font-size: 8px;">             APR 23 2004              U.S. PATENT &amp; TRADEMARK OFFICE           </div>				Application Number: 10/758,977	
				Filing Date: January 14, 2004	
				First Named Inventor: Kenneth S. McElvain	
				Art Unit: Not Yet Assigned	
				Examiner Name: Not Yet Assigned	
Sheet 1 of 1				Attorney Docket Number: 002986.P045	
<b>NON PATENT LITERATURE DOCUMENTS</b>					
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>		
		LYONS et al., "The Use of Triple-Modular Redundancy to Improve Computer Reliability," IBM Journal, April 1962, pp. 200-209.			
		HABINC, "Functional Triple Modular Redundancy (FTMR)" Gaisler Research, Version 0.2, December 2002, 56 pgs.			
		FRANCO, et al., "FPGA Implementation of a Serial Organized DA Multichannel FIR Filter," Tenth ACM International Symposium on Field Programmable Gate Arrays, Monterey, California, February 24-26, 2002.			
		HALPERIN, et al., "Combinatorial Approximation Algorithms for the Maximum Directed Cut Problem," Proceedings of 12 <sup>th</sup> Symposium on Discrete Algorithms, pp. 1-7, 2001.			
		PARHI, Keshab K., "VLSI digital signal processing systems: design and implementation," Wiley-Interscience, pp. 91-118 & 149-187, 1999.			
		THORNTON, James E., "Parallel Operation in the Control Data 6600," AFIPS Proc. FJCC, pt. 2, vol. 26, pp. 33-40, 1964.			

Examiner Signature	Date Considered
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<sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>Applicant is to place a check mark here if English Translation is attached.

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